

	Document ID	Issue Date	Pages	Title	Current OR
1	US 20050102545 A1	20050512	33	Synchronized communication between integrated circuit chips	713/401
2	US 20020094055 A1	20020718	11	Unified digital architecture	375/376
3	US 20010048388 A1	20011206	18	Autonomous hardwired tracking loop coprocessor for GPS and WAAS receiver	342/357.12
4	US 6480150 B2	20021112	19	Autonomous hardwired tracking loop coprocessor for GPS and WAAS receiver	342/357.12
5	US 6278403 B1	20010821	18	Autonomous hardwired tracking loop coprocessor for GPS and WAAS receiver	342/357.12
6	US 6038254 A	20000314	17	Adaptive filtering method and apparatus to compensate for a frequency difference between two clock sources	375/232
7	US 5859881 A	19990112	16	Adaptive filtering method and apparatus to compensate for a frequency difference between two clock sources	375/350
8	US 5757297 A	19980526	21	Method and apparatus for recovering a serial data stream using a local clock	341/100
9	US 5469516 A	19951121	20	Method for generating and storing digitized density thresholds for rastering a half tone original image	382/254
10	US 4408165 A	19831004	8	Digital phase detector	327/7
11	US 4283662 A	19810811	9	Line scan circuits for cathode ray tube displays	315/364
12	US 4053929 A	19771011	22	Contour fitting pictorial tracking gate	348/170
13	US 3950752 A	19760413	18	Navigation receiver system for synchronizing to noisy, asymmetrical signals	342/396
14	US 3896443 A	19750722	18	OMEGA NAVIGATION RECEIVER STATION PAIR SELECTION AND LOP DISPLAY SYSTEM	342/396
15	EP 143920 A2	19850612	7	Receiving device for a high rate bit-serial asynchronous data link.	
16	DE 3335417 A	19850418	8	Receiver for asynchronous serial high bit rate data - has tunable oscillator for matching phase shift of incoming signal.	

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1		Clavequin, Jean-Paul et al.	US 20050102545
2		Cranford, Hayden Clavie JR. et al.	US 20020094055
3		Falk, Henry D. et al.	US 20010048388
4	375/344; 701/213	Falk; Henry D. et al.	US 6480150
5	375/344; 701/213	Peng; Leon Kuo- Liang et al.	US 6278403
6	327/39; 375/327; 375/362	Ferraiolo; Frank David et al.	US 6038254
7	341/100; 375/333; 375/375; 375/376	Ferraiolo; Frank David et al.	US 5859881
8	341/101	Ferraiolo; Frank David et al.	US 5757297
9	358/3.06; 358/3.14; 358/536	Kerz; Ludo	US 5469516
10	327/12	Braun; Reinhold	US 4408165
11	348/500; 348/540	Ainscow; Frank et al.	US 4283662
12	250/203.5	Collins, III; Robert H. et al.	US 4053929
13	324/76.79	Zitzow; Harold P. et al.	US 3950752
14	324/76.79	Zitzow; Harold P.	US 3896443
15	370/212; 370/FOR. 157	WAHR, ALFONS DIPL-INF et al.	EP 143920 A2
16		BREINTNER, R et al.	DE 3335417 A1

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1	US 20040114675 A1	20040617	32	OFDM pilot tone tracking for wireless LAN	375/149
2	US 20030165209 A1	20030904	21	Phase-interpolator based PLL frequency synthesizer	375/376
3	US 20020176483 A1	20021128	32	OFDM pilot tone tracking for wireless LAN	375/137
4	US 20020146084 A1	20021010	12	Apparatus and method for oversampling with evenly spaced samples	375/360
5	US 20020136343 A1	20020926	36	Analog unidirectional serial link architecture	375/376
6	US 20020094055 A1	20020718	11	Unified digital architecture	375/376
7	US 6738608 B2	20040518	14	Frequency-timing control loop for wireless communication systems	455/260
8	US 5612975 A	19970318	21	Digital receiver for variable data rate communications	375/319

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1	375/260; 375/327	Crawford, James A.
2		Chen, Chun-Ying et al.
3	370/208; 375/376	Crawford, James A.
4	375/376	Cranford, Hayden Clavie JR. et al.
5		Cranford, Hayden Clavie JR. et al.
6		Cranford, Hayden Clavie JR. et al.
7	375/316; 375/327; 455/255; 455/258	Black; Peter J. et al.
8	329/308; 341/61; 375/235; 375/327; 375/343; 375/349; 375/350; 455/307; 455/337; 708/314	Becker; Donald W. et al.